

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	(*6741729*).PN.	USPAT; USOCR; EPO; JPO	OR	OFF	2004/08/20 17:31
S2	1	(*6125192*).PN.	USPAT; USOCR; EPO; JPO	OR	OFF	2004/08/20 17:31
S3	82	(340/5.72.ccls. *70*/\$.ccls.) and (electronic adj lock\$3) and ((electronic adj key) fob smartcard) and switch\$4	USPAT; EPO; JPO	OR	OFF	2005/03/17 15:53
S4	1	S3 and ((SMD) (surface adj mount\$3 adj device))	USPAT; EPO; JPO	OR	OFF	2005/03/17 15:50
S5	69	(340/5.72.ccls. *70*/\$.ccls.) and (electronic adj lock\$3) and ((electronic adj key) fob smartcard) and switch\$4 and block\$3	USPAT; EPO; JPO	OR	OFF	2005/03/17 15:55
S6	1580	watanuki.in.	USPAT; EPO; JPO	OR	OFF	2005/03/17 15:55
S7	2	S6 and (electronic adj lock)	USPAT; EPO; JPO	OR	OFF	2005/03/17 16:00
S8	1	*6095415*.PN.	USPAT; USOCR	OR	OFF	2005/03/17 15:55
S9	1	*4716748*.PN.	USPAT; USOCR	OR	OFF	2005/03/17 15:56
S10	1	*5982295*.PN.	USPAT; USOCR	OR	OFF	2005/03/17 15:56
S11	1	*5982295*.PN.	USPAT; USOCR	OR	OFF	2005/03/17 15:56
S12	1	*6003349*.PN.	USPAT; USOCR	OR	OFF	2005/03/17 15:59
S13	1	*5816086*.PN.	USPAT; USOCR	OR	OFF	2005/03/17 15:59
S14	1	*5570599*.PN.	USPAT; USOCR	OR	OFF	2005/03/17 16:00
S15	0	(*70*/\$.ccls. and 340/5.7.ccls.) and (electronic adj lock\$3) and ((SMD) (surface adj mount\$3 adj device)) and rotor and (load\$break\$3 adj switch) and (block\$3 adj slide) and ((PCB) (printed adj circuit adj board))	USPAT; EPO; JPO	OR	ON	2005/03/17 16:05
S16	0	(*70*/\$.ccls. 340/5.7.ccls.) and (electronic adj lock\$3) and ((SMD) (surface adj mount\$3 adj device)) and rotor and (load\$break\$3 adj switch) and (block\$3 adj slide) and ((PCB) (printed adj circuit adj board))	USPAT; EPO; JPO	OR	ON	2005/03/17 16:05
S17	0	(electronic adj lock\$3) and ((SMD) (surface adj mount\$3 adj device)) and rotor and (load\$break\$3 adj switch) and (block\$3 adj slide) and ((PCB) (printed adj circuit adj board))	USPAT; EPO; JPO	OR	ON	2005/03/17 16:06

S18	0	(electronic adj lock\$3) and ((SMD) (surface adj mount\$3 adj device)) and (rotor rotat\$6 transition\$7 (load\$break\$3 adj switch)) and (block\$3 adj slide) and ((PCB) (printed adj circuit adj board))	USPAT; EPO; JPO	OR	ON	2005/03/17 16:07
S19	0	(70/249 70/252-256 70/278-279) and (electronic adj lock\$3) and (rotor rotat\$6 transition\$7 (load\$break\$3 adj switch)) and (block\$3 adj slide) and ((PCB) (printed adj circuit adj board))	USPAT; EPO; JPO	OR	ON	2005/03/17 16:08
S20	0	(70/249 70/252-256 70/278-279) and (electronic adj lock\$3) and (load\$break\$3 adj switch) and (block\$3 adj slide)	USPAT; EPO; JPO	OR	ON	2005/03/17 16:08
S21	0	(70/249 70/252-256 70/278-279) and (electronic adj lock\$3)	USPAT; EPO; JPO	OR	ON	2005/03/17 16:09
S22	5	(70/249.ccls. 70/256.ccls. 70/279.ccls.) and (electronic adj lock\$3)	USPAT; EPO; JPO	OR	ON	2005/03/17 16:10
S23	9	340/5.31.ccls. and electronic adj (lock\$3 key) and (rotat\$7 transition\$7) and block\$3	USPAT; EPO; JPO	OR	ON	2005/03/17 16:11